



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Thompson, et al.**

Application Serial No.: **10/660,154**

Filed: **September 10, 2003**

Title: **Multi-Mode Bias Circuit for Power Amplifiers**

TRANSMITTAL OF FORMAL DRAWINGS

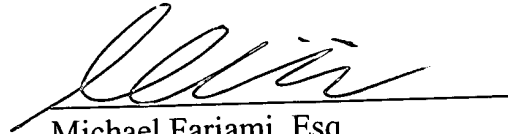
Commissioner for Patents
Official Draftsperson
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

Enclosed, please find three (3) sheets of formal drawings for the above-identified application, which include the designation "Prior Art" in Figures 1 and 2 as requested by the Examiner. Issue fee payment has been submitted under separate cover. An early issuance of the above-identified application is respectfully requested.

Respectfully Submitted;
FARJAMI & FARJAMI LLP

Dated: 1/17/05


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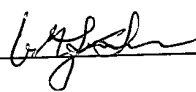
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